## THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	§	Patent No.:	6875635	
CHENG MAN HON, et al.	<b>§</b>	_		
	§ §	<b>Issue Date:</b>	4/5/2005	
Application No.: 10/811581	§			
	§	Examiner:	Jasmind Jhihan B. Clark	
<b>Filed:</b> 3/29/2004	§			
	§	§ Group Art Unit: 2811 §		
Docket No.: SC11852HP D01	§			
	§			
Title: METHOD OF ATTACHING A	DIE TO	) A SUBSTRAT	E	
		Certificate of Submission		
		I hereby certify that this correspondence is being submitted to the U.S.P.T.O., Alexandria, VA.  Addressed per C.F.R.§ 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.		
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		Facsimile transmitted in accordance with C.F.R.§1.6(d).		
		<u>X</u> Submitted electronically via EFS in accordance wit "Legal Framework for EFS Web".		
		98.06		
		Date of Submission  Signature		
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		Printed Name of Person Signing Certificate		

Commissioner for Patents Alexandria, VA 22313

## SUBMISSION OF CERTIFICATE OF CORRECTION

## Dear Commissioner:

Enclosed is a Certificate of Correction listing error(s) in the subject patent. Please enter these corrections. Since the errors appear to be on the part of the United States Patent Office, there should be no charge.

9/08/06 Date Respectfully submitted,

Joanna G. Chiu

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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION Page <u>1</u> of <u>1</u> 6875635 PATENT NO.: 10/811581 APPLICATION NO: 4/5/2005 DATE: INVENTOR(S): CHENG MAN HON It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below: In Column 6, Line 17, Claim No. 1: Change "treater" to - - greater - -.

MAILING ADDRESS OF SENDER (Please do not use customer number below)

Freescale Semiconductor, Inc. Law Department 7700 West Parmer Lane PL02 Austin, TX 78729